

Development of organic-inorganic halide perovskites (OHPs) based memristors

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Abstract

Organic – inorganic halide perovskite (OHP) has drawn researcher's attention working in the field of optoelectronics from last ten years due to its remarkable optical properties such as adjustable band gap, ambipolar charge transport, high optical absorption coefficients, and extended carrier diffusion lengths. OHP based memristors (memory + resistors) are newly introduced passive two-terminal, nonlinear device used for information storage. In this work we have fabricated methyl ammonium lead iodide (MAPbI3) crystals-based device (Figure 5) from MAPbI3 crystals (Figure 1). The crystals have been developed by simple solution process-based method. Developed crystals have shown highly porous geometry and trap charges across these pores facilitates higher conductivity. The fabricated device exhibit ~2.7 milli second response recovery time (Figure 7) which enable elevated speed and shown hysteresis in the I-V characteristics (Figure 8) thus demonstrated superior storage capacities. Hence developed device has been the potential candidates for next-generation nonvolatile memories.

1. Introduction/Objective

MAPbl3 has been the well known OHP in the field of thin film-based photovoltaic device (PVD). However, shape and size of crystals have profound impacts on electrical and optical properties. Hence lower dimension (zero-dimension, one dimension) OHP crystal structure provide particular set of advantages such as large surface to volume ratio, better charge separation and conductivity makes OHP very useful in optoelectronics application other then PVD. The morphological, and structural properties of the one-dimensional perovskite micro-rods have been examined using SEM and XRD characterization techniques. Memory resistors has been fabricated using micro-rods. Current – Voltage (IV) characteristics has been studied to examine charge conduction behavior of the device

4. Methods



1M solutions of Pbl, and MAI were prepared separately in DMF and IPA

2. Hypothesis

If one-dimensional MAPbl₃ perovskite micro-rods has been obtained by solution-based facile synthesis of, non-volatile memory may be improved further.

3. Materials and Equipment

Materials Required	Equipment to be Use
1. Lead (II) iodide	1. Heater with stirrer
2. methylammonium iodide	2. Ozone cleaner
3. Dimethylformamide (DMF)	3. Dry nitrogen blow
4. isopropanol (IPA)	4. Magnifying glass
	5. FESEM
5. ITO coated glass	6. Solar Simulator wi
6. Silver Paste (Ag)	SMU
7. Acetone, Methanol, Hellmanex	7. XRD machine
III solution.	<i>°</i> 8. Ultra sonicator

- *i* dryer
- vith Keithley 2400

9. Kickstart software

4.2 Device Fabrication:



ITO coated glass substrate has been cleaned using Acetone, Methanol, and Hellmanex-3 cleaning solution under ultrasonication sequentially



After drying under nitrogen each MAPbl3 has place at ITO substrate



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silver (Ag) paste has been used to connect both the ends to complete Ag-MAPbI3-Ag device



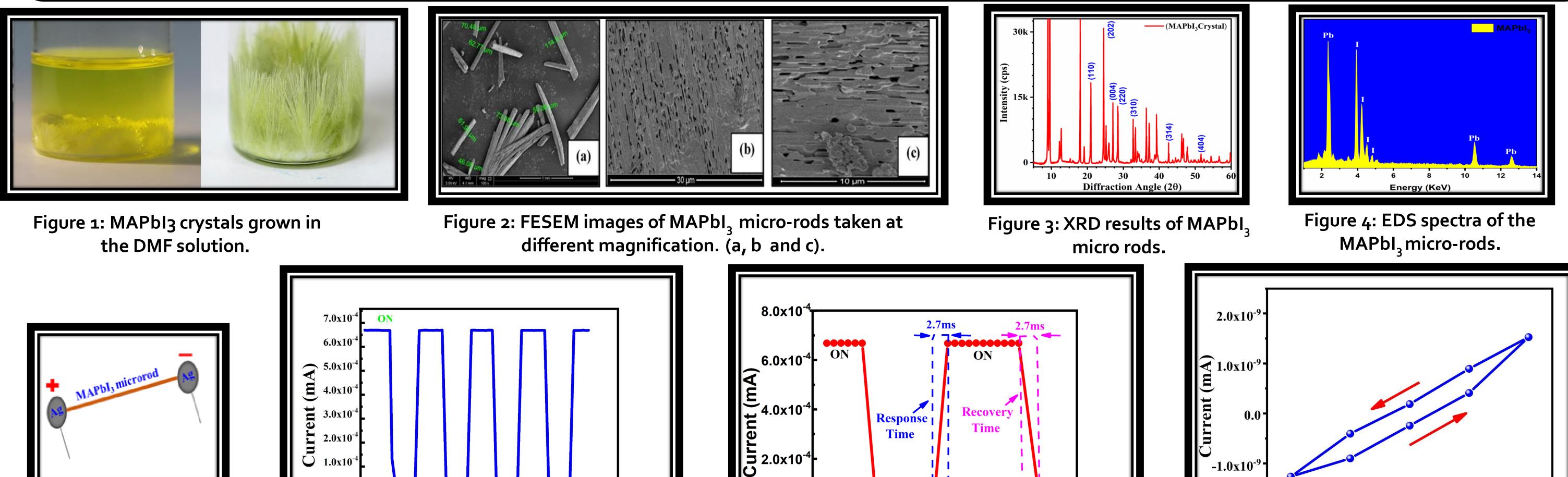
Current-voltage (I-V) characteristics of the device has been measured under dark and 1 Sun

Pbl, and MAI solutions were mixed in the 2:1 volumetric ratio then simultaneous heating and stirring at 70 °C gives a clear yellow solution of the MAPbl₂

The solutions of MAPbl₃ was left to cool down slowly at a very slow rate of 10°C/HR)

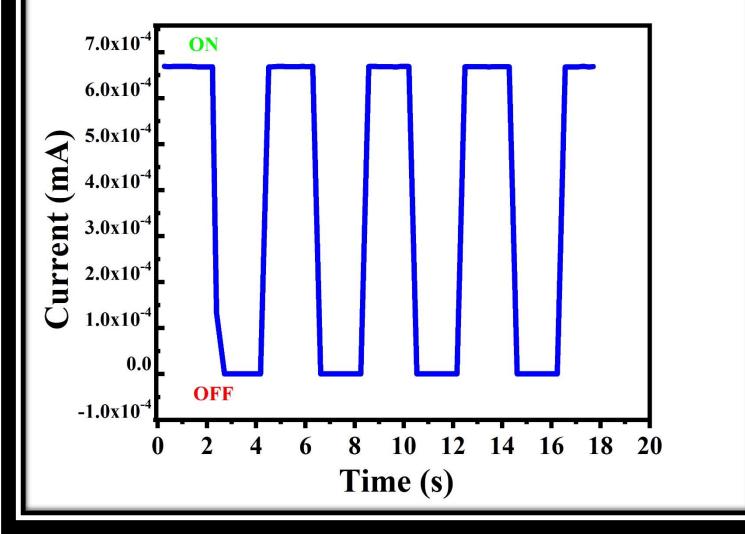
The growth of the micro-rods were observed after many hours of cooling down solution to room temperature.

5. Results and Analysis



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Figure 5: Device Structure of Ag/MAPbI₃/Ag based photo-induced memory resistor.







Voltage (V)

Figure 6: ON/OFF behaviour of Ag/MAPbl3/Ag based memory resistor.

5. Concluding remarks

X-ray diffraction and EDs analyses confirm phase purity and high crystallinity of the developed micro-rods. The morphological studies (SEM) confirm crack free morphology of MAPbl₃ micro-rods with porous structure. Developed crystals have shown highly porous geometry and trap charges across these pores facilitates higher conductivity.

The fabricated device exhibit ~2.7 ms response recovery time which enable elevated speed and shown hysteresis in I-V scan thus demonstrated superior storage capacities. Hence developed device has been the potential candidates for next-generation non-volatile memories.

Figure 7: Response and recovery switching cycles of device.

Time (s)

OFF

6. References

OFF

- Figure 8: Hysteresis plot for Ag/MAPbl3/Ag based device.
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-2.0x10

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