

Received July 8, 2021, accepted July 30, 2021, date of publication August 11, 2021, date of current version August 18, 2021. *Digital Object Identifier* 10.1109/ACCESS.2021.3104184

A Fast Convergent Homotopy Perturbation Method for Solving Selective Harmonics Elimination PWM Problem in Multi Level Inverter

SALMAN AHMAD[®]¹, (Member, IEEE), ATIF IQBAL[®]², (Senior Member, IEEE), MOHAMMAD ALI[®]³, (Member, IEEE), KHALIQUR RAHMAN[®]³, AND ABDELLAHI SIDI AHMED[®]², (Member, IEEE)

¹Department of Electrical Engineering, Islamic University of Science and Technology, Awantipora, Jammu and Kashmir 192122, India

²Department of Electrical Engineering, Qatar University, Doha, Qatar

³Department of Electrical Engineering, Zakir Husain College of Engineering and Technology, Aligarh Muslim University, Aligarh 202001, India

Corresponding author: Abdellahi Sidi Ahmed (as095223@qu.edu.qa)

This publication was made possible by Qatar University-Marubeni Concept to Prototype Development Research grant #[M-CTPCENG-2020-2] from the Qatar University. The statements made herein are solely the responsibility of the authors. The APC of the paper is funded by the Qatar National Library, Doha, Qatar.

ABSTRACT Pulse width modulation (PWM) control for power converters have been vastly investigated and used in many industrial application. For medium voltage and high power applications, low switching frequency PWM techniques are preferred over high switching frequency-based PWM techniques. The preprogrammed low switching frequency-based PWM technique known as selective harmonics elimination (SHE) gives the better quality waveform at a lower switching frequency. The main difficulty in applying SHE PWM is in solving of non-linear transcendental equations for obtaining switching angles. Several methods have been proposed for computation of switching angles that include numerical techniques, optimization techniques, and algebraic techniques. However, the computation of switching angles is still a challenging task in the application of SHE PWM. In this paper, a novel fast convergent homotopy perturbation method (HPM) is proposed to compute switching angles for a multilevel inverter at a faster rate. The solutions obtained by the proposed technique is as accurate as obtained by the algebraic methods with no dependency on the initial guess. The proposed technique can compute the higher number of switching angles with multiple solutions in some modulation index range. A prototype is developed and the computed switching angles have been verified using field-programmable gate arrays (FPGA) controller to validate the results for practical applications.

INDEX TERMS Multilevel inverter, modulation index, selective harmonics elimination, pulse width modulation, homotopy perturbation method, FPGA.

I. INTRODUCTION

Pulse width Modulation (PWM) techniques have been extensively investigated and used for the efficient operation of power electronics converter in the past. It produces desired fundamental component at the output with minimum undesired harmonics components [1]. However, the main focus was on high switching frequency-based PWM techniques, such as carrier-based modulation (SPWM) and space vectorbased modulation (SVPWM). In SPWM, a high-frequency carrier is continuously compared with a fundamental component (desired component), and at the intersection points,

The associate editor coordinating the review of this manuscript and approving it for publication was Zhilei Yao^(D).

the pulses are generated. In space vector PWM, the distinct switching states are first identified, and then these vectors are applied to obtain the desired output. The main advantage of high switching frequency (in kHz) techniques includes having the desired output components along with shifting of harmonics component at switching frequency as sideband and thus higher switching frequency will result in the minimum filtering [1]–[3] requirement. In the past, two-level voltage source inverters (VSI's) have been mainly used, and various PWM techniques have been implemented for twolevel VSI's. However, higher power processing requirement creates the opportunity to develop new converter topologies to meet the increasing power demands with limited semiconductor device voltage and current ratings.

The main multilevel topologies such as cascaded H-bridge (CHB), neutral point clamped (NPC), flying capacitor (FC), active neutral point clamped (ANPC), modular multilevel converter (MMC), and various reduced device count emerging topologies have been investigated and developed in the recent years [4], [5]. The essential features of these topologies include better quality output waveforms (Stepped) and thus lower total harmonics distortion (THD), lower devise switching frequency, use of low voltage and current rating semiconductor switches, and production of small dv/dt at switches [5]. Since the output waveform's quality is kept as per IEEE 591 standard, the high switching frequency-based PWM techniques are preferred for the operation and control of these multilevel inverter topologies [2]. However, in high-power application of multilevel converter, switching losses also need to be considered, affecting efficiency considerably. In this context, the low switching frequency based PWM techniques such as selective harmonics elimination (SHE), selective harmonics mitigation (SHM), optimum pulse width modulation techniques (OPWM), synchronous optimum pulse width modulation (SOPWM), pulse width amplitude modulation and THD minimization PWM techniques [6]-[12]. These methods are referred to as the pre-programmed PWM techniques as it is optimized as per requirement, such as eliminating particular harmonics, minimizing specific harmonics to particular level, and controlling overall harmonics content in the output waveform [13]-[15]. In SHE-PWM, the lower order harmonics are considered for complete elimination while maintaining the required fundamental component simultaneously [16]. The number of switching angles considered in quarter-wave will decide the number of harmonics that can be eliminated from the output waveform. More the number of switching angles is considered, more harmonics are eliminated from the output waveform but at the cost of higher switching losses [17]. Thus, it is a trade-off between switching loss and output power quality. The non-transcendental SHE equations, obtained after Fourier series analysis, provide different solutions as a modulation index function: a unique solution, multiple solutions, and no solution [18]. The multiple solutions in some modulation index range give different THD, and the following non eliminated harmonics in the output waveform. Thus it is essential to find all the solutions from the derived system of non-linear non-transcendental equations [19].

To solve the system of simultaneous SHE equations, several methods have been proposed in literature over the years [9], [20]. These can be broadly classified as numerical techniques-based iterative methods, metaheuristic-based optimizations methods, and algebraic methods [21]. The numerical-based iterative techniques are fast convergent, and solutions up to the desired accuracy are obtained. However, the main challenge in utilizing these techniques involves the proper selection of initial guesses and computation of derivatives in every iteration, which results in divergence in the solution and singularity problems [22], [23]. In metaheuristic techniques, a single objective function is solved with

VOLUME 9, 202

different constraints on the harmonics levels. The objective function is optimized with modern techniques such as particle swarm optimization (PSO), modified PSO, genetic algorithm (GA), differential evolution (DE), ant colony optimization (ACO), artificial bee colony (ABC), teaching-learning (TL) optimization, hybrid PSO and GA optimization, grey wolf optimization etc. [24]-[29]. The main challenge in utilizing a metaheuristic-based optimization technique includes a slow convergence rate, proper selection of algorithm parameters, initial guess, and more computational time for the entire modulation index range [24], [25], [30]-[33]. In algebraic methods, the SHE equations are transformed into algebraic equations using the trigonometric formulas and then solved using the Walsh method, symmetric polynomial methods, and Groebner bases [20]. The algebraic methods are capable of giving all the solutions from the SHE equation with exact values. However, the polynomial degree increases many folds with an increase in the number of switching angles, and thus these methods are used only for computation of few switching angles [22]. The capacitor voltage balancing using SHE-PWM for low device count multilevel topologies have been addressed in [34]. The real application of SHE-PWM have been successfully implemented using artificial neural network (ANN), Data fitting, hopfield neural network, etc. [35]–[39]. The switching angles first computed using any of the method discussed above and then real time implementation for various application is applied.

This paper proposes a novel homotopy perturbation method (HPM) for the computation of switching angles in SHE-PWM. The proposed method is accurate, robust, fast convergent, and capable of computing all the switching angles with any random initial guess. Various multilevel waveforms such as three-level waveforms, multilevel stepped waveforms have been considered, and the switching angles have been computed. The accurate multiple solutions with a faster computational algorithm confirm the robust operation of the proposed HPM technique. The obtained solutions are as exact as obtained by the algebraic methods but can solve for more number of switching angles as compared to algebraic methods. The harmonics profile for various modulation indexes is given to confirm the accuracy of the computational results. The sensitivity analyses of the computed solutions are also given with comparative analysis of different methods. The Computational results are verified by the hardware results using field-programmable gate arrays (FPGA) for various cases. The dynamic load change results for resistive and resistive-inductive load have been given for 11-level stepped waveform with variation in load from no-load to half-load to full load.

The paper is organized as follows: In section II, mathematical modeling and problem formulation for three-level and the multilevel stepped waveform is discussed, in section III, HPM computational procedure is discussed, in section IV, the selected computational results of switching angle trajectories for various cases by the proposed HPM technique is given, in Section V, sensitivity analysis is given, in section VI,







FIGURE 1. (a) Power circuit of a single-phase cascaded H-bridge inverter, (b) Stepped output phase voltage.

laboratory setup of multilevel inverter and digital control is discussed. Also, selected hardware results are given for resistive resistive-inductive loads, and finally, in section VII conclusion of the work is given.

II. PROBLEM FORMULATION

The main objective in SHE PWM is to obtain switching angles so that the desired fundamental frequency component is achieved and the elimination of specific low order harmonics components from the output waveform. The set of simultaneous mathematical equations are obtained after applying the Fourier series decomposition of the output waveform, and then homotopy perturbation is implemented to calculate the switching angles. The generalized cascaded H-bridge multilevel inverter circuit and a quarter-wave generalized symmetrical output voltage waveform are shown in Fig.1(a) and Fig.1(b). Three voltage levels such as +Vdc, 0, or -Vdc

are obtained from proper switching of the semiconductor switches from individual H-cells. The number of positive and negative pulses in a cycle determines the possible number of harmonics for elimination. Fourier series expansion is utilized to analyze the output waveform shown in Fig. 1 (b). The general Fourier series expansion of the stepped waveform shown in Fig.1 (b) is given by (1).

$$f(t) = a_0 + \sum_{n=1}^{\infty} (a_n \cos(n\omega t) + b_n \sin(n\omega t))$$
(1)

For simplicity, the waveforms are considered to have odd quarter-wave symmetry. In such a scenario, the Fourier coefficients a0 and an, are zero, and the even harmonics components will be zero. The Fourier coefficient bn, after simplification, is given by (2), where n represents harmonic order and N total number of switching transitions in quarter periods.

$$b_n = \begin{cases} \frac{4V_{dc}}{n\pi} \sum_{k=1}^{N} (-1)^{k+1} \cos(n\alpha_k), & \text{for odd } n\\ 0, & \text{for even } n \end{cases}$$
(2)

And for staircase waveform, from cascaded H bridge inverter it will be given by (3).

$$b_n = \begin{cases} \frac{4V_{dc}}{n\pi} \sum_{k=1}^{N} \cos(n\alpha_k), & \text{for odd } n\\ 0, & \text{for even } n \end{cases}$$
(3)

The triplen harmonics for a balanced three-phase system automatically will get canceled. So the output waveform can be considered as having the desired fundamental component and several nontriplen odd-order harmonics components and can be expressed by (4) and (5) for three-level waveform and multilevel waveforms.

$$V_{\text{out}} = \sum_{n=1}^{\infty} \left[\frac{4V_{dc}}{n\pi} \sum_{k=1,5,7...}^{N} (-1)^{k+1} \cos(n\alpha_k) \right] \sin(n\omega t) \quad (4)$$

$$V_{\text{out}} = \sum_{n=1}^{\infty} \left[\frac{4V_{dc}}{n\pi} \sum_{k=1,5,7\dots}^{N} \cos(n\alpha_k) \right] \sin(n\omega t)$$
(5)

The N-1 harmonics can be eliminated from the output waveform (usually the lower order) if N number of switching transitions are considered quarterly. One degree of freedom is reserved for obtaining the desired fundamental component magnitude. The generalized expression for having desired fundamental component and elimination of specific low order harmonics components eliminated from the output waveform in three-level and multilevel stepped waveforms are given in (6) and (7), respectively.

$$\sum_{k=1}^{N} (-1)^{k+1} \cos \alpha_k - M = 0$$

$$\sum_{k=1}^{N} (-1)^{k+1} \cos (5\alpha_k) = 0$$

...

$$\sum_{k=1}^{N} (-1)^{k+1} \cos (n\alpha_k) = 0$$
(6)

$$\sum_{k=1}^{N} \cos \alpha_{k} - S \times M = 0$$

$$\sum_{k=1}^{N} \cos (5\alpha_{k}) = 0$$

$$\cdots$$

$$\sum_{k=1}^{N} \cos (n\alpha_{k}) = 0$$
(7)

S is the number of steps, and M represents the modulation index. The ratio between the fundamental component's magnitude and the maximum achievable magnitude is given by (8).

$$M = \frac{V_1}{V_{1\,\text{max}}} \tag{8}$$

The above equations must satisfy the minimum, maximum and non-equality constraints, expressed in (9).

$$0 < \alpha_1 < \alpha_2 < \alpha_3 \cdots \alpha_N < \frac{\pi}{2} \tag{9}$$

The equations in (6) and (7) are considered simultaneous, highly non-linear, and transcendental. Their solution may give multiple solutions, unique solution or no solution at different modulation index values and is therefore not straightforward.

III. HPM SOLVING PROCEDURE

An advanced technique is proposed here to obtain all the possible solutions from the expressions (6) and (7) which are further rewritten in matrix form as:

$$f(\alpha_1, \alpha_2, \alpha_3 \cdots, \alpha_n) = \begin{bmatrix} f_1(\alpha_1, \alpha_2, \alpha_3 \cdots, \alpha_n) \\ f_2(\alpha_1, \alpha_2, \alpha_3 \cdots, \alpha_n) \\ \vdots \\ f_n(\alpha_1, \alpha_2, \alpha_3 \cdots, \alpha_n) \end{bmatrix}$$
(10)

where, $f_k : \mathbb{R}^n \to \mathbb{R}$. Also let the switching angles, $\alpha \in \mathbb{R}^n$. In vector form α is expressed as (11), where $\alpha_k \in \mathbb{R}$ and k = 1, 2...n.

$$\alpha = \begin{bmatrix} \alpha_1 \\ \alpha_2 \\ \vdots \\ \alpha_n \end{bmatrix}$$
(11)

Then in the homotopy perturbation method, the systems of equations (let us say two functions in two variables) which are considered to be solved can be rearranged as given in (12):

$$F(\alpha) = \begin{cases} f_1(\alpha) = 0; \\ f_2(\alpha) = 0; \end{cases} \qquad \alpha = (\alpha_1, \alpha_2)^T \in \mathbb{R}^2 \quad (12)$$

where, $f_1, f_2 : \mathbb{R}^2 \to \mathbb{R}$, and let $\alpha^* = (\alpha_1, \alpha_2)^T$ makes () to zero, that is, it is the solution of the problem. Let $\gamma = (\gamma_1, \gamma_2)^T$, be the initial guess which is very close to the actual solution α^* . The Taylor series expansion of (1) near γ will result in (13).

$$F(\alpha) = \begin{cases} f_1(\gamma) + (\alpha_1 - \gamma_1) f_{1,\alpha_1}(\gamma) + (\alpha_2 - \gamma_2) f_{1,\alpha_2}(\gamma) \\ + F_1(\alpha) = 0 \\ f_2(\gamma) + (\alpha_1 - \gamma_1) f_{2,\alpha_1}(\gamma) + (\alpha_2 - \gamma_2) f_{2,\alpha_2}(\gamma) \\ + F_2(\alpha) = 0 \end{cases}$$
(13)

where, $F_1(\alpha) = f_1(\alpha) - f_1(\gamma) - (\alpha_1 - \gamma_1) f_{1,\alpha_1}(\gamma) + (\alpha_2 - \gamma_2) f_{1,\alpha_2}(\gamma)$ and $F_2(\alpha) = f_2(\alpha) - f_2(\gamma) - (\alpha_1 - \gamma_1) f_{2,\alpha_1}(\gamma) + (\alpha_2 - \gamma_2) f_{2,\alpha_2}(\gamma)$. The expression in (13) can be written in the following form as:

$$\begin{cases} \alpha_{1}f_{1,\alpha_{1}}(\gamma) + \alpha_{2}f_{1,\alpha_{2}}(\gamma) \\ = \gamma_{1}f_{1,\alpha_{1}}(\gamma) + \gamma_{2}f_{1,\alpha_{1}}(\gamma) - f_{1}(\gamma) - F_{1}(\alpha) \\ \alpha_{1}f_{2,\alpha_{1}}(\gamma) + \alpha_{2}f_{2,\alpha_{2}}(\gamma) \\ = \gamma_{1}f_{2,\alpha_{1}}(\gamma) + \gamma_{2}f_{2,\alpha_{1}}(\gamma) - f_{2}(\gamma) - F_{2}(\alpha) \end{cases}$$
(14)

After solving (14), (15) is obtained, which represents the solution for the switching angles.

$$\begin{bmatrix} \alpha_1 \\ \alpha_2 \end{bmatrix} = \gamma - J^{-1} (f_1, f_2) (\gamma) \left(\begin{bmatrix} f_1(\gamma) \\ f_2(\gamma) \end{bmatrix} + \begin{bmatrix} F_1(\alpha) \\ F_2(\alpha) \end{bmatrix} \right)$$
(15)

where, inverse Jacobian is:

$$J^{-1}(f_1, f_2)(\gamma) = \begin{bmatrix} f_1, \alpha_1(\gamma) & f_1, \alpha_2(\gamma) \\ f_2, \alpha_1(\gamma) & f_2, \alpha_2(\gamma) \end{bmatrix}$$
(16)

In homotopy perturbation method, the construction of Homotopy function for (14) is $H(\bar{\alpha}, \lambda) : R^2 \times [0, 1] \rightarrow R^2$ and it should satisfy (11) and thus (17) is obtained as:

$$H(\bar{\alpha}, \lambda) = \begin{bmatrix} \bar{\alpha}_1 \\ \bar{\alpha}_2 \end{bmatrix} - \gamma - J^{-1} (f_1, f_2) (\gamma) \left(\begin{bmatrix} f_1(\gamma) \\ f_2(\gamma) \end{bmatrix} + \lambda \begin{bmatrix} F_1(\bar{\alpha}) \\ F_2(\bar{\alpha}) \end{bmatrix} \right) = 0$$
(17)

where, λ is the embedding parameter and after substituting (18) is obtained as:

$$H(\bar{\alpha}, 0) = \begin{bmatrix} \bar{\alpha}_1 \\ \bar{\alpha}_2 \end{bmatrix} - \gamma - J^{-1} (f_1, f_2) (\gamma) \left(\begin{bmatrix} f_1(\gamma) \\ f_2(\gamma) \end{bmatrix} \right) = 0$$
(18)
$$H(\bar{\alpha}, 1) = \begin{bmatrix} \overline{\alpha}_1 \\ \bar{\alpha}_2 \end{bmatrix} - \gamma - J^{-1}$$

$$\times (f_1, f_2) (\gamma) \left(\begin{bmatrix} f_1(\gamma) \\ f_2(\gamma) \end{bmatrix} + \begin{bmatrix} F_1(\bar{\alpha}) \\ F_2(\bar{\alpha}) \end{bmatrix} \right) = 0 (19)$$

Here the embedding parameter λ increases monotonically from 0 to 1 as trivial problem and $H(\bar{\alpha}, 0)$ is continuously deformed to main formulation $H(\bar{\alpha}, 1) = F(\bar{\alpha}) = 0$. The λ is used as expanding parameter to get (20) in HPM.

$$\bar{\alpha} = \begin{bmatrix} \alpha_{1,0} \\ \alpha_{2,0} \end{bmatrix} + \lambda \begin{bmatrix} \alpha_{1,1} \\ \alpha_{2,1} \end{bmatrix} + \lambda^2 \begin{bmatrix} \alpha_{1,2} \\ \alpha_{2,2} \end{bmatrix}$$
(20)

The approximate solution of (3) is then obtained as in (21).

$$\alpha^* = \lim_{\lambda \to 1} \bar{\alpha} = \begin{bmatrix} \alpha_{1,0} \\ \alpha_{2,0} \end{bmatrix} + \begin{bmatrix} \alpha_{1,1} \\ \alpha_{2,1} \end{bmatrix} + \begin{bmatrix} \alpha_{1,2} \\ \alpha_{2,2} \end{bmatrix}$$
(21)

The convergence of series in (20) is given in (III). The application of HPM for (21) can be obtained from the above expression as (23), shown at the bottom of the next page:

$$\alpha^* = \lim_{\lambda \to 1} \bar{\alpha} = \begin{bmatrix} \alpha_{1,0} \\ \alpha_{2,0} \end{bmatrix} + \begin{bmatrix} \alpha_{1,1} \\ \alpha_{2,1} \end{bmatrix} + \begin{bmatrix} \alpha_{1,2} \\ \alpha_{2,2} \end{bmatrix}$$
(22)

By equating the terms of identical power of λ , the following expressions are obtained in (26)–(28):

$$\lambda^{0} : \begin{bmatrix} \alpha_{1,0} \\ \alpha_{2,0} \end{bmatrix} = \lambda - J^{-1} \left(f_{1}, f_{2} \right) \left(\gamma \right) \left(\begin{bmatrix} f_{1}(\gamma) \\ f_{2}(\gamma) \end{bmatrix} \right)$$
(26)

$$\lambda^{1}: \begin{bmatrix} \alpha_{1,1} \\ \alpha_{2,1} \end{bmatrix} = -J^{-1} \left(f_{1}, f_{2} \right) \left(\gamma \right) \left(\begin{bmatrix} F_{1} \left(\alpha_{0} \right) \\ F_{2} \left(\alpha_{0} \right) \end{bmatrix} \right)$$
(27)

$$\lambda^{2} : \begin{bmatrix} \alpha_{1,2} \\ \alpha_{2,2} \end{bmatrix} = -J^{-1} (f_{1}, f_{2}) (\gamma) \\ \times \left(\begin{bmatrix} \alpha_{1,1}F_{1,\alpha_{1}} (\alpha_{0}) + \alpha_{2,1}F_{1,\alpha_{2}} (\alpha_{0}) \\ \alpha_{11}F_{2,\alpha_{1}} (\alpha_{0}) + \alpha_{2,1}F_{2\alpha_{2}} (\alpha_{0}) \end{bmatrix} \right)$$
(28)

After substituting the above expressions in (20), it will result in (24), as shown at the bottom of the page, and simplification will result in (25), as shown at the bottom of the page. The above formulation allows the following two iterative methods to solve the system of non-linear equations given as:

- i. For a given $\theta_n = \begin{bmatrix} p_n \\ q_n \end{bmatrix}$ calculate the approximate solution $\theta_{n+1} = \begin{bmatrix} p_{n+1} \\ q_{n+1} \end{bmatrix}$: $\theta_{n+1} = \theta_n J^{-1} (f_1, f_2) (\theta_n) F (\theta_n)$ ii. or a given $\theta_n = \begin{bmatrix} p_n \\ q_n \end{bmatrix}$ calculate the approximate solution

$$\begin{aligned} \theta_{n+1} &= \left\lfloor \begin{array}{c} p_{n+1} \\ q_{n+1} \end{array} \right\rfloor : \theta_{n+1} = \theta_n - J^{-1} \left(f_1, f_2 \right) \left(\theta_n \right) \left(F \left(\theta_n \right) \\ &+ \left[\begin{array}{c} F_1 \left(\theta_n - J^{-1} \left(f_1, f_2 \right) \left(\theta_n \right) F \left(\theta_n \right) \\ F_2 \left(\theta_n - J^{-1} \left(f_1, f_2 \right) \left(\theta_n \right) F \left(\theta_n \right) \end{array} \right] \right) \end{aligned}$$

The flowchart for computing switching angles, storing results and computing total harmonics distortion for each solution set is shown in Fig. (2). The software code is developed and run for each cases to obtain the accurate switching angles for any voltage source converter topology. The scheme can also be implemented for reduce device count topologies.



FIGURE 2. HPM flowchart in solving system of nonlinear simultaneous harmonics equations.

IV. COMPUTATIONAL RESULTS BY HPM

The proposed HPM technique discussed in the previous section is used to compute the switching angles. Various cases of stepped waveform of multilevel inverter have been considered, and the selected results have been given to confirm the capability of the proposed techniques to compute

$$\begin{bmatrix} \alpha_{1,0} + \lambda \alpha_{1,1} + \lambda^{2} \alpha_{1,2} + \cdots \\ \alpha_{1,0} + \lambda \alpha_{1,1} + \lambda^{2} \alpha_{1,2} + \cdots \end{bmatrix} - \gamma - J^{-1} (f_{1}, f_{2}) (\gamma) \begin{bmatrix} f_{1}(\gamma) \\ f_{2}(\gamma) \end{bmatrix} - \lambda J^{-1} (f_{1}, f_{2}) (\gamma)$$

$$\times \begin{bmatrix} F_{1} (\alpha_{0}) + (\lambda \alpha_{1,1} + \lambda^{2} \alpha_{1,2} + \cdots) F_{1,\alpha_{1}} (\alpha_{0}) + \\ (\lambda \alpha_{2,1} + \lambda^{2} \alpha_{2,2} + \cdots) F_{1,\alpha_{2}} (\alpha_{0}) + \cdots \\ F_{2} (\alpha_{0}) + (\lambda \alpha_{1,1} + \lambda^{2} \alpha_{1,2} + \cdots) F_{2,\alpha_{1}} (\alpha_{0}) + \\ (\lambda \alpha_{2,1} + \lambda^{2} \alpha_{2,2} + \cdots) F_{2,\alpha_{2}} (\alpha_{0}) + \cdots \end{bmatrix} = 0$$

$$\alpha^{*} = \begin{bmatrix} \alpha_{1,0} \\ \alpha_{2,0} \end{bmatrix} + \begin{bmatrix} \alpha_{1,1} \\ \alpha_{2,1} \end{bmatrix} + \begin{bmatrix} \alpha_{1,2} \\ \alpha_{2,2} \end{bmatrix} + \cdots = \gamma - J^{-1} (f_{1}, f_{2}) (\gamma) \begin{bmatrix} f_{1}(\gamma) \\ f_{2}(\gamma) \end{bmatrix} - J^{-1} (f_{1}, f_{2}) (\gamma) \begin{bmatrix} F_{1} (\alpha_{0}) \\ F_{2} (\alpha_{0}) \end{bmatrix}$$

$$-J^{-1} (f_{1}, f_{2}) (\gamma) \left(\begin{bmatrix} \alpha_{1,1} F_{1,\alpha_{1}} (\alpha_{0}) + \alpha_{2,1} F_{1,\alpha_{2}} (\alpha_{0}) \\ \alpha_{1,1} F_{2,\alpha_{1}} (\alpha_{0}) + \alpha_{2,1} F_{2,\alpha_{2}} (\alpha_{0}) \end{bmatrix} \right) - \cdots$$

$$(24)$$

$$\alpha^{*} = \gamma - J^{-1} (f_{1}, f_{2}) (\gamma) \begin{bmatrix} f_{1}(\gamma) \\ f_{2}(\gamma) \end{bmatrix} - J^{-1} (f_{1}, f_{2}) \begin{bmatrix} F_{1} \left(\gamma - J^{-1} (f_{1}, f_{2}) (\gamma) \begin{bmatrix} f_{1}(\gamma) \\ f_{2}(\gamma) \end{bmatrix} \\ F_{2} \left(\gamma - J^{-1} (f_{1}, f_{2}) (\gamma) \begin{bmatrix} f_{1}(\gamma) \\ f_{2}(\gamma) \end{bmatrix} \end{bmatrix} + \cdots$$
(25)

f

IEEEAccess



FIGURE 3. Switching angles as function of modulation index for stepped waveform at N = 3, 5, 7, 9, 11, 13.

 TABLE 1. Convergence rate for various modulation index and maximum error in SHE function.

Modulation Index	Iterations	Max. error	Time (S)
$M_1 = 0.680$	8	1.02×10^{-7}	0.72
$M_2 = 0.728$	9	2.97×10^{-14}	0.81
$M_3 = 0.797$	9	1.41×10^{-7}	0.83
$M_4 = 0.836$	8	1.77×10^{-10}	0.75
$M_5 = 0.8460$	14	4.50×10^{-7}	1.02

the exact solutions at a fast convergent rate with negligible dependence on the initial guess. The lower order, nontriplen harmonics are considered for elimination from the output waveform since the triplen harmonics will automatically get canceled from the line voltage in a three-phase balanced system. The modulation index represents the desired fundamental component of the output voltage. For stepped waveforms of 7, 11, 15, 19, 23, 27 levels, the switching angles trajectories as a modulation index function are shown in Fig. 3. The solution trajectories confirm the multiple solutions, no solution and unique solution in a wide modulation index range with accuracy in solutions as close to as obtained in algebraic methods. Similarly, the solution trajectories for three-level waveforms for 3, 5, 7, 11, and 13 switching angles in a quarter period are shown in Fig. 4. For three level three phase inverter the switching angles lies in the range of [0, 0.9323], [0, 0.9187], [0, 0.9137], [0, 0.9113], [0, 0.90910], [0, 0.9088] for N = 3, 5, 7, 9, 11, 13 respectively. While for three phase stepped waveform, the switching angles ranges as [0.5853, 0.9229], [0.3759, 0.8464], [0.4660, 0.8594], [0.4865, 0.8303], [0.4909, 0.8116], [0.5228, 0.7948] for N = 3, 5, 7, 9, 11, 13 respectively. The solution trajectory shows various numbers of solutions as a function of



FIGURE 4. Switching angles as function of modulation index for three-level waveform at N = 3, 5, 7, 9, 11, 13.



FIGURE 5. Normalized harmonics profile at (a) N = 3, M = 0.8323.

the modulation index. It again confirms the proposed technique's capability to compute all the possible switching angle solutions from the output waveform. The multiple switching angles for stepped waveforms to accommodate more harmonics can also be evaluated using the proposed technique. However, it will increase the switching frequency of the system. Similarly for asymmetrical multilevel inverter cases that are different dc-link voltages can also be computed by the proposed technique. The proposed technique can also compute a higher number of switching angles. The Normalized harmonics profile for the stepped waveform of the 11-level stepped waveform, that is, N = 5 at $M_1 = 0.680$, $M_2 =$ 0.728, $M_3 = 0.797$, $M_4 = 0.836$, $M_5 = 0.8460$, is shown in Fig. 5. The lower order targeted harmonics, i.e., 5th, 7th,



FIGURE 6. % Line voltage THD for 11-level CHB inverter.



FIGURE 7. Convergence of various methods and proposed method.

11th, and 13th, are entirely removed from the output waveform. The maximum error in the harmonics equations defined in (7) has values of 1.02×10^{-7} , 2.97×10^{-14} , 1.41×10^{-7} , 1.77×10^{-10} , and 4.50×10^{-7} , respectively. The convergence rate or the number of iterations for the HPM algorithm to achieve this accuracy is 8, 9, 9, 8, and 14, respectively. The convergent rates, maximum error and time of computation for various modulation indexes are shown in Table 1. The total harmonics distortion (%THD) in line voltage for 11level cascaded H-bridge inverter is shown in Fig. (6), which has been computed using (29), and it indicates that different solutions for a particular modulation index have different values. Therefore the optimum solution may be selected based on the minimum %THD value of the solution.

$$THD = \sqrt{\left[\sum_{n=s,7}^{200} \left(\frac{H_n}{H_1}\right)^2\right]} \times 100$$
(29)

V. SENSITIVITY ANALYSIS OF THE SOLUTIONS

For sensitivity analysis, the derivatives of the objective function are considered. Then the effect of one or multiple parameter's variations on the system performance can easily be



FIGURE 8. (a) Schematic and (b) Actual experimental setup.

(b)

assessed using the following expression:

$$\begin{aligned} f\left(\alpha_{1}+\Delta\alpha_{1},\ldots\alpha_{n}+\Delta\alpha_{n}\right) \\ \approx f\left(\alpha_{1},\ldots\alpha_{n}\right)+\frac{\partial f}{\partial\alpha_{1}}\Delta\alpha_{1}+\ldots+\frac{\partial f}{\partial\alpha_{1}}\Delta\alpha_{n} \end{aligned} (30)$$

Here the objective function is continuous and differentiable at all points in the defined space, which is a crucial hypothesis in the sensitivity analysis. The SHE PWM techniques' performance under variation in switching angle values from the computed values is essential. The harmonics distortion's sensitivity is determined by having a slight variation in switching angles from the optimum solution obtained from the differential evolution-based optimization technique in a broader range of solutions.

The second and first-order performance function derivatives are utilized to assess parameter variation on performance by second-order sensitivity analysis. It increases the accuracy in sensitivity analysis, and unlike the first-order derivative, the optimal point does not necessarily converge to zero points. For multivariable, the use of Taylor series expansion by considering both first and second-order terms results as:

$$f(\alpha_1 + \Delta \alpha_1, \dots, \alpha_n + \Delta \alpha_n) \\\approx f(\alpha_1, \dots, \alpha_n) + \frac{\partial f}{\partial \alpha_1} \Delta \alpha_1$$



FIGURE 9. Experimental result (a) N = 5 (b) N = 7 (c) N = 9 (d) N = 13.

$$+\dots+\frac{\partial f}{\partial \alpha_{n}}\Delta \alpha_{n}+\frac{1}{2}\frac{\partial^{2} f}{\partial \alpha_{1}^{2}}\Delta \alpha_{1}^{2}+\dots+\frac{1}{2}\frac{\partial^{2} f}{\partial \alpha_{n}^{2}}\Delta \alpha_{n}^{2}$$
$$+\frac{1}{2}\frac{\partial^{2} f}{\partial \alpha_{1}^{2}}\Delta \alpha_{1}^{2}+\frac{\partial^{2} f}{\partial \alpha_{1} \partial \alpha_{2}}\Delta \alpha_{1}\Delta \alpha_{2}+\dots+\frac{\partial^{2} f}{\partial \alpha_{1} \partial \alpha_{n}}\Delta \alpha_{1}\Delta \alpha_{n}$$
$$+\frac{\partial^{2} f}{\partial \alpha_{2} \partial \alpha_{3}}\Delta \alpha_{2}\Delta \alpha_{3}+\dots+\frac{\partial^{2} f}{\partial \alpha_{n-1} \partial \alpha_{n}}\Delta \alpha_{n-1}\Delta \alpha_{n} \qquad (31)$$

The effect of slight variations in the switching angles to the harmonic distortion is determined and compared after the optimum swathing angles are obtained. The desired equation for computation of desired fundamental and undesired low order harmonics is given as:

$$V_{1} = \left| \frac{4V_{dc}}{\pi} \left(\cos\left(\alpha_{1}\right) + \cos\left(\alpha_{2}\right) \right. \\ \left. + \cdots + \cos\left(\alpha_{N-1}\right) + \cos\left(\alpha_{N}\right) \right) \right. \\ V_{n} = 0 = \left| \frac{4V_{dc}}{n\pi} \left(\cos\left(n\alpha_{1}\right) + \cos\left(n\alpha_{2}\right) \right. \\ \left. + \cdots + \cos\left(n\alpha_{N-1}\right) + \cos\left(n\alpha_{N}\right) \right)$$
(32)

In (32), V_1 is the fundamental voltage, which is the desired part and, n_1 , n_2 , n_3 ... are the targeted harmonics for elimination. For a 5-variable case, the 5th, 7th, 11th, and 13th order harmonics are chosen as these are the lowest order harmonics in a balanced 3-phase system. The optimized solution to (32) is considered for validating the results from both the optimization and sensitivity analyses. The switching angles for a harmonics minimization scheme with five switching angles considered for sensitivity analysis, the objective function (OF) shown in (33) is used in [40].

$$F = \min_{\alpha_k} \left\{ \left(100 \times \frac{V_1^{\circ} - V_1}{V_1^{*}} \right)^4 + \sum_{k=2}^N \frac{1}{l_k} \left(50 \times \frac{V_{h_t}}{V_1} \right)^2 \right\};$$

$$k = 1, 2, 3 \cdots, N \quad (33)$$

The elimination of 5th, 7th, 11th, and 13th orders are considered and are given as:

$$R = \min_{\alpha_2} \left\{ \sum_{k=2}^{N} \frac{1}{h_k} \left(\frac{V_{h_k}}{V_1} \right)^2 \right\}; \quad k = 1, 2, \cdots, 5 \quad (34)$$

For the exact computed switching angles $(\alpha_1, \alpha_2, \dots \alpha_5)$, R is zero, as shown in the table. However, in the practical implementation, a small deviation in these switching angles will result in a deviation in R from zero. The sensitivity analysis described is used to calculate R's deviation with different %age deviations in switching angles. The incremental error per degree of deviation in switching angles is shown in Table 2. All first and second-order derivatives are calculated, and then R is represented by the expansion given in (31) for the variations in the switching angle $\Delta \alpha$ to limit the harmonics distortion within the specified range can be evaluated. If a value HR = $\sqrt{2}$ (and let $HR \le 2\%$) as a design constraint. Hence the maximum permitted error in switching angle $\Delta \alpha$ can be derived from (30) and (31). At the optimum

TABLE 2. The analytical derivatives.

A. By analytical technique					
R	$\partial/\partial \alpha_1$	$\partial/\partial \alpha_2$	$\partial/\partial \alpha_3$	$\partial/\partial \alpha_4$	$\partial/\partial \alpha_5$
1	0	0	0	0	0
$\partial/\partial \alpha_1$	7.7	4.2	3.0	-6.1	-7.1
$\partial/\partial \alpha_2$	8.0	4.15	3.65	-7.88	-1.81
$\partial/\partial \alpha_3$	-5.52	-2.21	-3.87	4.05	3.57
$\partial/\partial \alpha_4$	-4.35	-3.04	1.77	4.87	5.25
$\partial/\partial lpha_5$	-6.05	2.77	-1.25	5.33	-3.42
B. From the sensitivity analysis					
R	$\partial/\partial \alpha_1$	$\partial/\partial lpha_2$	$\partial/\partial lpha_3$	$\partial/\partial \alpha_4$	$\partial/\partial lpha_5$
1	0	0	0.1	-0.2	0.1
$\partial/\partial \alpha_1$	7.05	4.01	3.44	-6.48	-7.77
$\partial/\partial lpha_2$	7.150	4.65	3.21	-7.12	-2.6
$\partial/\partial lpha_3$	-5.11	-2.15	-3.33	4.11	3.74
$\partial/\partial \alpha_4$	-3.16	-4.27	2.87	4.05	5.77
$\partial/\partial lpha_5$	-6.88	2.12	-2.71	4.6	-3.05

point, the partial second derivative is given as:

$$\Delta R = \sum_{i=1,j=1,i-j}^{N} \left(R_{\alpha,\alpha_i} \right) \Delta \alpha^2 + \sum_{i=1,j=1,i\neq j}^{N} \left[R_{\alpha,\alpha} \right] (\pm \Delta \alpha) (\pm \Delta \alpha) \quad (35)$$

where,

$$R_{\alpha_i \alpha_j} = \left. \frac{\partial^2}{\partial \alpha_i \partial \alpha_j} R \right|_{\text{optimum}} , \quad i \neq j$$

and,

$$R_{\alpha_i\alpha_i} = \left. \frac{1}{2} \frac{\partial^2}{\partial \alpha_i^2} R \right|_{\text{optimum}} , \quad i = j$$

The worst-case situation from smallest variation in $\Delta \alpha$ is assured in (36) by having the max function, which occurs from the proper selection of + and - signs. The various sensitivity cases are given in Table 3.

$$|\Delta \alpha_{\max}| \leq \sqrt{\frac{R^{\max}}{\max\left(\sum_{i=1,j=1,i=j}^{N} R_{\alpha_{j}\alpha_{j}} + \sum_{i=1,j=1,i\neq j}^{N} (\pm R_{\alpha_{i}\alpha_{j}})\right)}}$$
(36)

VI. EXPERIMENTAL VALIDATION

The computed switching angles by HPM discussed in the previous sections have been validated by hardware developed in the laboratory. The schematic and complete hardware setup of power rating of 1.5 kW is shown in Fig. 8(a) and Fig. 8(b), respectively. The field-programmable gate array (FPGA) from VIRTEX-5 XC5VLX50T is used as a digital controller as it has a sizable amount of logic components and area unit programmable logic devices. The control

Worst case switching angles (rad.) combination	
$\Delta \alpha_1 = -0.0015, \Delta \alpha_2 = -0.0015, \Delta \alpha_3 =$	
$-0.0015, \Delta \alpha_4 = 0.0015, \Delta \alpha_5 = 0.0015$	
The corresponding HR:	
From the first order estimation 0.001%	
From the method in [45] 1.41%	
From the proposed method 1.05%	



FIGURE 10. Harmonics spectrum (a) N = 5 (b) N = 7 (c) N = 9 (d) N = 13.

TABLE 4. P	rototype	components	rating.
------------	----------	------------	---------

Components	Ratings
SIC-MOSFET $(S_{x1} - S_{x4})$	FDP19N40 (1200V, 40A)
DC Power supply	TDK-Lambda GEN (0-100V, 15 A)
Controller	Virtex-5 FPGA
Switching frequency (f_{sw})	50 Hz
Load	1.5 kW

code for switching devices is converted into very high definition language (VHDL) by using the Xilinx logic blocks compatible with Simulink library to generate gate pulses. The individual logic elements connections are programmed by VHDL and create a logical function, i.e., digital hardware. Since at present, the available microcontrollers are capable of providing only six pairs of PWM signals, which is not sufficient for multilevel converter operation. However, the FPGA may provide multiple PWM signals useful in a multilevel converter system. Also, with the clock signal, an FPGA may run all the operations in parallel, and hence it updates all gate signals simultaneously, which is an advantage over a digital signal processor (DSP). There are one or more LUTs on each flip-flop of PLB. There are many



FIGURE 11. Harmonics spectrum (a) 11-level voltage and current waveform at R load (b) 11-level voltage and current waveform at RL load (c) voltage harmonics profile at M = 0.80 (d) voltage harmonics profile at M = 0.82.



FIGURE 12. Practical results (a) Output voltage and current waveform at high inductance RL load (b) Dynamic response for changing load.

vendors such as Xilinx, Altera, Atmel, Lattice, etc., who provide a wide range of FPGAs. The complete hardware setup includes the multilevel converter with gate drivers, programmable dc supply, FPGA controller, oscilloscope, and harmonics analyzer is shown in Fig. 8(b). The components ratings of the prototype developed in the laboratory is given in Table 4. For various switching angle cases and modulation indexes, the experimental results have been obtained. The concept is validated through selected results shown in Fig. 9 for three-level waveforms for different switching angles in a quarter-period while considering eliminating lower-order non-triplen harmonics for elimination. The corresponding harmonics profiles are given in Fig. 10. The harmonics profiles shows that the targeted harmonic order are absent in the output voltage waveform. The voltage and current waveform for resistive and resistive-inductive load is shown in Fig. 11(a) and Fig. 11(b) for M = 0.80 and M = 0.82, respectively. The corresponding harmonics profiles for output voltage are shown in Fig. 11(c) and Fig. 11(d), respectively. The targeted harmonics for elimination are absent in the harmonics profile. Thus, the experimental results exhibit that the harmonics elimination's hardware results are in agreement with the computational results.

VII. CONCLUSION

A novel fast convergent homotopy perturbation method was proposed in this paper to solve the selective-harmonic elimination-based pulse width modulation problem. The generalized mathematical modeling was first established for three-level and stepped waveforms. The proposed technique have been implemented to solve the highly non-linear transcendental system of equations. The selected computed results for switching angle trajectories as a function of modulation index were given for three level and stepped waveforms for various switching angle cases. The number of iterations and computational time have been reduced drastically in the proposed technique. Different numbers of solutions were obtained as the modulation index varies and thus offers increased degree of freedom. The obtained solutions were as accurate as of the algebraic methods, which were confirmed from the output waveform's harmonic profile. Moreover, in algebraic methods, the number of switching angle computations is limited. A prototype was developed in the laboratory to validate the switching angles. An FPGA controller was used to generate the gate pulses, and the harmonic analyzer was employed to capture the harmonics profile. The dynamic performance have been shown by suddenly

changing the load from no load to half load to full load for highly inductive RL load. The hardware results commend the computational results.

REFERENCES

- D. Holmes and T. Lipo, Pulse Width Modulation for Power Converters Principles and Practice. New York, NY, USA: IEEE, 2003.
- [2] B. Wu, High-Power Converters and AC Drives. Hoboken, NJ, USA: Wiley, 2006.
- [3] F. Wang, "Sine-triangle versus space-vector modulation for three-level PWM voltage-source inverters," *IEEE Trans. Ind. Appl.*, vol. 38, no. 2, pp. 500–506, Mar./Apr. 2002.
- [4] J. Rodriguez, S. Bernet, B. Wu, J. O. Pontt, and S. Kouro, "Multilevel voltage-source-converter topologies for industrial medium-voltage drives," *IEEE Trans. Ind. Electron.*, vol. 54, no. 6, pp. 2930–2945, Dec. 2007.
- [5] S. Gonzalez, S. Verne, and M. Valla, *Multilevel Converters for Industrial Applications*. Boca Raton, FL, USA: CRC Press, 2016.
- [6] H. S. Patel and R. G. Hoft, "Generalized techniques of harmonic elimination and voltage control in thyristor inverters: Part I–Harmonic elimination," *IEEE Trans. Ind. Appl.*, vol. IA-9, no. 3, pp. 310–317, May 1973.
- [7] H. S. Patel and R. G. Hoft, "Generalized techniques of harmonic elimination and voltage control in thyristor inverters: Part II–harmonic elimination," *IEEE Trans. Ind. Appl*, vol. IA-9, no. 3, pp. 310–317, May 1973.
- [8] M. Tayyab, A. Sarwar, M. Tariq, R. K. Chakrabortty, and M. J. Ryan, "Hardware-in-the-loop implementation of projectile target search algorithm for selective harmonic elimination in a 3-Phase multilevel converter," *IEEE Access*, vol. 9, pp. 30626–30635, 2021.
- [9] M. S. A. Dahidah, G. Konstantinou, and V. G. Agelidis, "A review of multilevel selective harmonic elimination PWM: Formulations, solving algorithms, implementation and applications," *IEEE Trans. Power Electron.*, vol. 30, no. 8, pp. 4091–4106, Aug. 2015.
- [10] J. Sun, S. Beineke, and H. Grotstollen, "Optimal PWM based on real-time solution of harmonic elimination equations," *IEEE Trans. Power Electron.*, vol. 11, no. 4, pp. 612–621, Jul. 1996.
- [11] A. Edpuganti and A. K. Rathore, "A survey of low switching frequency modulation techniques for medium-voltage multilevel converters," *IEEE Trans. Ind. Appl.*, vol. 51, no. 5, pp. 4212–4228, Sep./Oct. 2015.
- [12] S. Ahmad, I. Khan, A. Iqbal, and S. Rahman, "A novel pulse width amplitude modulation for elimination of multiple harmonics in asymmetrical multilevel inverter," in *Proc. IEEE Texas Power Energy Conf. (TPEC)*, Feb. 2021, pp. 1–6.
- [13] S. Ahmad, A. Iqbal, and M.-H. I. Ashraf, Selected Harmonics Elimination in Multilevel Inverter Using Improved Numerical Technique. Doha, Qatar: IEEE CPE-POWERENG, Apr. 2018.
- [14] S. Ahmad, I. Ashraf, A. Iqbal, and M. A. A. Fatimi, "SHE PWM for multilevel inverter using modified NR and pattern generation for wide range of solutions," in *Proc. IEEE 12th Int. Conf. Compat., Power Electron. Power Eng. (CPE-POWERENG)*, Apr. 2018, pp. 1–6.
- [15] M. D. Siddique, S. Mekhilef, N. M. Shah, A. Sarwar, A. Iqbal, M. Tayyab, and M. K. Ansari, "Low switching frequency based asymmetrical multilevel inverter topology with reduced switch count," *IEEE Access*, vol. 7, pp. 86374–86383, 2019.
- [16] S. Sirisukprasert, J.-S. Lai, and T.-H. Liu, "Optimum harmonic reduction with a wide range of modulation indexes for multilevel converters," *IEEE Trans. Ind. Electron.*, vol. 49, no. 4, pp. 875–881, Aug. 2002.
- [17] S. Kundu, S. Bhowmick, and S. Banerjee, "Improvement of power utilisation capability for a three-phase seven-level CHB inverter using an improved selective harmonic elimination–PWM scheme by sharing a desired proportion of power among the H-bridge cells," *IET Power Electron.*, vol. 12, no. 12, pp. 3242–3253, Oct. 2019.
- [18] S. Ahmad, M. Meraj, A. Iqbal, and I. Ashraf, "Selective harmonics elimination in multilevel inverter by a derivative-free iterative method under varying voltage condition," *ISA Trans.*, vol. 92, pp. 241–256, Sep. 2019.
- [19] M. Al-Hitmi, S. Ahmad, A. Iqbal, S. Padmanaban, and I. Ashraf, "Selective harmonic elimination in a wide modulation range using modified Newton–Raphson and pattern generation methods for a multilevel inverter," *Energies*, vol. 11, no. 2, p. 458, Feb. 2018.
- [20] A. M. Amjad and Z. Salam, "A review of soft computing methods for harmonics elimination PWM for inverters in renewable energy conversion systems," *Renew. Sustain. Energy Rev.*, vol. 33, pp. 141–153, May 2014.

- [21] Z. Yuan, R. Yuan, W. Yu, J. Yuan, and J. Wang, "A Groebner bases theorybased method for selective harmonic elimination," *IEEE Trans. Power Electron.*, vol. 30, no. 12, pp. 6581–6592, Dec. 2015.
- [22] K. Yang, Q. Zhang, R. Yuan, W. Yu, J. Yuan, and J. Wang, "Selective harmonic elimination with Groebner bases and symmetric polynomials," *IEEE Trans. Power Electron.*, vol. 31, no. 4, pp. 2742–2752, Apr. 2016.
- [23] S. Ahmad, M. Al-Hitmi, A. Iqal, I. Ashraf, M. Meraj, and S. Padmanaban, "Low-order harmonics control in staircase waveform useful in high-power application by a novel technique," *Int. Trans. Electr. Energy Syst.*, vol. 29, no. 3, Mar. 2019, Art. no. e2769.
- [24] A. Kavousi, B. Vahidi, R. Salehi, M. K. Bakhshizadeh, N. Farokhnia, and S. Hamid Fathi, "Application of the bee algorithm for selective harmonic elimination strategy in multilevel inverters," *IEEE Trans. Power Electron.*, vol. 27, no. 4, pp. 1689–1696, Apr. 2012.
- [25] K. Sundareswaran, K. Jayant, and T. N. Shanavas, "Inverter harmonic elimination through a colony of continuously exploring ants," *IEEE Trans. Ind. Electron.*, vol. 54, no. 5, pp. 2558–2565, Oct. 2007.
- [26] B. Ozpineci, L. M. Tolbert, and J. N. Chiasson, "Harmonic optimization of multilevel converters using genetic algorithms," *IEEE Power Electron Lett.*, vol. 3, no. 3, pp. 92–95, Sep. 2005.
- [27] K. Ganesan, K. Barathi, P. Chandrasekar, and D. Balaji, "Selective harmonic elimination of cascaded multilevel inverter using BAT algorithm," *Proceedia Technol.*, vol. 21, pp. 651–657, Jan. 2015.
- [28] M. H. Etesami, N. Farokhnia, and S. H. Fathi, "Colonial competitive algorithm development toward harmonic minimization in multilevel inverters," *IEEE Trans. Ind. Informat.*, vol. 11, no. 2, pp. 459–466, Apr. 2015.
- [29] T. Babu, K. Priya, D. Maheswaran, K. Kumar, and N. Rajasekar, "Selective voltage harmonic elimination in PWM inverter using bacterial foraging algorithm," *Swarm Evol. Comput.*, vol. 20, pp. 74–81, Feb. 2015.
- [30] M. Kumari, M. Ali, S. Ahmad, I. Ashraf, A. Azeem, M. Tariq, M. S. B. Arif, and A. Iqbal, "Genetic algorithm based SHE-PWM for 1ø and 3- ø voltage source inverters," in *Proc. Int. Conf. Power Electron.*, *Control Autom. (ICPECA)*, Nov. 2019, pp. 1–6.
- [31] S. Ahmad, M. Al-Hitmi, A. Iqbal, K. Rahman, and I. Ashraf, "Low switching frequency modulation of a 3 × 3 matrix converter in UPFC application using differential evolution method," *Int. Trans. Electr. Energy Syst.*, vol. 30, no. 1, Jan. 2020, Art. no. e12179.
- [32] M. T. Hagh, H. Taghizadeh, and K. Razi, "Harmonic minimization in multilevel inverters using modified species-based particle swarm optimization," *IEEE Trans. Power Electron.*, vol. 24, no. 10, pp. 2259–2267, Oct. 2009.
- [33] M. A. Memon, M. D. Siddique, M. Saad, and M. Mubin, "Asynchronous particle swarm optimization-genetic algorithm (APSO-GA) based selective harmonic elimination in a cascaded H-bridge multilevel inverter," *IEEE Trans. Ind. Electron.*, early access, Feb. 25, 2021, doi: 10.1109/TIE.2021.3060645.
- [34] M. Wu, Y. W. Li, and G. Konstantinou, "A comprehensive review of capacitor voltage balancing strategies for multilevel converters under selective harmonic elimination PWM," *IEEE Trans. Power Electron.*, vol. 36, no. 3, pp. 2748–2767, Mar. 2021.
- [35] F. Filho, L. Tolbert, C. Yue, and B. Ozpineci, "Real-time selective harmonic minimization for multilevel inverters connected to solar panels using artificial neural network angle generation," *IEEE Trans. Ind. Appl.*, vol. 47, no. 5, pp. 2117–2124, Sep./Oct. 2011.
- [36] M. Ahmed, A. Sheir, and M. Orabi, "Real-time solution and implementation of selective harmonic elimination of seven-level multilevel inverter," *IEEE J. Emerg. Sel. Topics Power Electron.*, vol. 5, no. 4, pp. 1700–1709, Dec. 2017.
- [37] M. Balasubramonian and V. Rajamani, "Design and real-time implementation of SHEPWM in single-phase inverter using generalized Hopfield neural network," *IEEE Trans. Ind. Electron.*, vol. 61, no. 11, pp. 6327–6336, Nov. 2014.
- [38] K. Yang, M. Feng, Y. Wang, X. Lan, J. Wang, D. Zhu, and W. Yu, "Realtime switching angle computation for selective harmonic control," *IEEE Trans. Power Electron.*, vol. 34, no. 8, pp. 8201–8212, Aug. 2019.
- [39] M. Ali, M. Tariq, K. A. Lodi, R. K. Chakrabortty, M. J. Ryan, B. Alamri, and B. C, "Robust ANN-based control of modified PUC-5 inverter for solar PV applications," *IEEE Trans. Ind. Appl.*, vol. 57, no. 7, pp. 3863–3876, Jul./Aug. 2021.
- [40] S. Ahmad and A. Iqbal, "Switching angles computations using PSO in selective harmonics minimization PWM," in *Metaheuristic and Evolutionary Computation: Algorithms and Applications*. Singapore: Springer, 2021, pp. 437–461.

IEEEAccess



SALMAN AHMAD (Member, IEEE) received the B.Tech. degree in electrical engineering from Aligarh Muslim University, Aligarh, India, in 2010, the M.Tech. degree from the Indian Institute of Technology, Roorkee, India, in 2012, and the Ph.D. degree in power electronics from Aligarh Muslim University, in 2020. He is currently an Assistant Professor with the Department of Electrical Engineering, Islamic University of Science and Technology, Awantipora, India. He worked as a

Lecturer with Debre Berhan University and Arba Minch University, Ethiopia, for more than three years. He has published more than 25 technical articles in different journals and conference proceedings and contributed four book chapters in edited books published by Elsevier, USA, and Springer Nature, Singapore, respectively. He received two SEED GRANTS projects under TEQIP-III, Government of India at IUST. His current research interests include power converters, PWM techniques, renewable energy, and distributed generation.

He has been an Associate Member of the Institutions of Engineers, since 2016. He has been a reviewer of many reputed journals of IEEE, Elsevier, and Wiley, since 2015. He was the Session Chair at ECCE Asia (IEEE) Conference held in Singapore in May 2021.



ATIF IQBAL (Senior Member, IEEE) received the B.Sc. and M.Sc. degrees in engineering (power system and drives) from Aligarh Muslim University (AMU), Aligarh, India, in 1991 and 1996, respectively, the Ph.D. degree from Liverpool John Moores University, Liverpool, U.K., in 2006, and the D.Sc. (Habilitation) degree in control, informatics, and electrical engineering from Gdansk University of Technology, in 2019. He is currently a Full Professor with the Department of Electrical

Engineering, Qatar University, and a former Full Professor with the Department of Electrical Engineering, AMU, where he has been employed as a Lecturer with the Department of Electrical Engineering, since 1991, and he also worked as a Full Professor, until August 2016. He was a recipient of Maulana Tufail Ahmad Gold Medal for standing first in the B.Sc. Engg. (electrical) exams from AMU, in 1991. He has published widely in international journals and conferences his research findings related to power electronics, variable speed drives, and renewable energy sources. He has authored/coauthored more than 450 research articles and four books and several chapters in edited books. He has supervised several large research and development projects worth more than multi-million USD. He has supervised and co-supervised several Ph.D. students. His research interests include smart grid, complex energy transition, active distribution networks, electric vehicles drivetrain, sustainable development and energy security, distributed energy generation, and multiphase motor drive systems.

He was a recipient of the Outstanding Faculty Merit Award in the academic year 2014–2015 and the Research Excellence Awards 2015 and 2019 from Qatar University, Doha, Qatar. He has received several best research papers awards at IEEE ICIT-2013, IET-SEISCON-2013, SIGMA 2018, IEEE CEN-CON 2019, IEEE ICIOT 2020, ICSTEESD-20, and Springer ICRP 2020.



MOHAMMAD ALI (Member, IEEE) was born in Tripoli, Libya, in 1983. He received the B.E., M.Tech., and Ph.D. degrees in electrical engineering from Aligarh Muslim University (AMU), Aligarh, in 2011, 2013, and 2019, respectively.

He is currently working as an Assistant Professor with the Department of Electrical Engineering, AMU, where he is working on developing and modulating novel asymmetrical multilevel converters for renewable energy applications. He is

also working on matrix converters when it is subjected to input side unbalancing. He has previously worked as a Senior Research Fellow for nine months and as an Assistant Professor with the Department of Electrical Engineering, from 2016 to 2019. He has also worked as a Visiting Researcher at Qatar University, in 2018. He has published various new multilevel topologies for patents and is the author of articles in IEEE TRANSACTIONS/journals and IET journals, and several international conference papers. He is also the author of two book chapters in *Power Electronics Handbook* (Elsevier). His research interests include ac–ac and dc–ac power converter topologies, their analysis and modulation, and their application in renewable energy systems connected to the grid or stand-alone systems.

Dr. Ali is a Life Member of SSI, India, and has delivered various lectures in national and international workshops.



KHALIQUR RAHMAN received the B.Tech. and M.Tech. degrees from Aligarh Muslim University (AMU), Aligarh, India, in 2008 and 2010, respectively, and the Ph.D. degree from Qatar University, Qatar, in 2018. He was a Lecturer with the Faculty of Electrical Engineering, AMU, from July 2010 to June 2012, where he is currently an Assistant Professor with the Electrical Engineering Department. His research interests include power electronics converters and drives and renewable energy resources.



ABDELLAHI SIDI AHMED (Member, IEEE) received the B.Sc. degree in electrical engineering from Qatar University, Doha, Qatar, in 2015, where he is currently pursuing the master's degree. In 2015, he worked as a Research Assistant for one year in an internal-granted project related to islanding problems in micro-grids. He is a Senior Electrical Engineer at Qatari Emiri Engineering Corporations (QECE), Qatar Armed Forces. His research interests include EV-grid integration,

smart grid, grid-connected DG renewable energy sources, and THD mitigation. He received the Best Student Employee Award for working as a Math Tutor and an Academic Support Advisor with the Student Learning Support Center (SLSC), Qatar University, from 2011 to 2014.

...